AMENDMENT UNDER 37 C.F.R. § 1.116 U.S. APPLN. NO. 09/699,553 ATTORNEY DOCKET NO. Q61563

## **AMENDMENTS TO THE SPECIFICATION**

## Page 27, first full paragraph:

As described above, the correlation value from the correlator #0 of the correlator blocks  $101_0$  is output during the (M-1)st period and is equal to  $\underline{C_0S_0 + C_KS_K + ... + C_{(M-1)K}S_{(M-1)K}}$   $\underline{C_KS_0 + C_{2K}S_K + ... + C_{(M-1)K}S_{(M-2)K} + C_{MK}S_{(M-1)K}}$ -and hence is equivalent to the correlation value of the 0th signature of the th-0th chip (Fig. 8a). This is expressed as  $D_0U_0$  using  $D_0$  indicating zero delay and the signature  $U_0$ .

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